

**Abstract**

The invention relates to an amplifier comprising amplification means (AM) comprising an input and an output,  
5 said amplification means (AM) comprising a switching output stage delivering at least one output signal (OUS) via said output,  
said amplification means being fed by power supply means (PSM)  
10 said amplifier further comprising compensation means (CM) providing a compensation signal (CS) derived from the power supply voltage (PSV) of the power supply means (PSM), said compensation signal (CS) comprising a substantially inverse representation of said power supply voltage (PSV) and  
15 said compensation signal (CS) being fed to said amplification means (AM).

According to the invention, an effective error compensation of the output switching stage may in practice be implemented by establishment of a compensation, which on  
20 a run-time basis is based on the voltage of the power supply currently applied in the output switching stage.